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## **CERTIFICATE OF MAILING**

I hereby certify that this paper is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Mail Stop RCE; Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450 on November 28, 2005

Judith C. Stoker

## IN THE UNITED STATES PATENT & TRADEMARK OFFICE

Applicant:

John G. Edelen et al.

Paper No.:

Serial No.:

10/655,363

Group Art Unit:

2861

Filed:

September 4, 2003

Examiner: Lamson D. Nguyen

For: N-Well and Other Implanted Temperature Sense Resistors in Inkjet Print Head Chips

## **REQUEST FOR RECONSIDERATION**

Mail Stop RCE Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

Dear Sir:

In response to the Advisory Action of October 12, 2005 and the Official Action dated June 28, 2005, Applicants request reconsideration of the patentability of claims 1-31 and consideration of the following remarks (a request for continued examination is being filed herewith):

Remarks begin on page 2 of this paper.